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Yoshinari Oji	naka, Kanagawa, JAPAN ma, Kanagawa, JAPAN;							
JAPAN 2003	ATA ***********************************	J		-				
Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged Examiner's Signature Allowance Examiner's Signature ADDRESS			STATE OR COUNTRY JAPAN	SHEETS DRAWING 9		TOTAL CLAIMS 20		INDEPENDENT CLAIMS 6
22850 TITLE Semiconductor inte	grated circuit including op	eration te	est circuit and o	operatio	on test i	method t	hereo	f
RECEIVED No	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT No for following:			All Fees 1.16 Fees (Filing) 1.17 Fees (Processing Ext. of time) 1.18 Fees (Issue) Other Credit				